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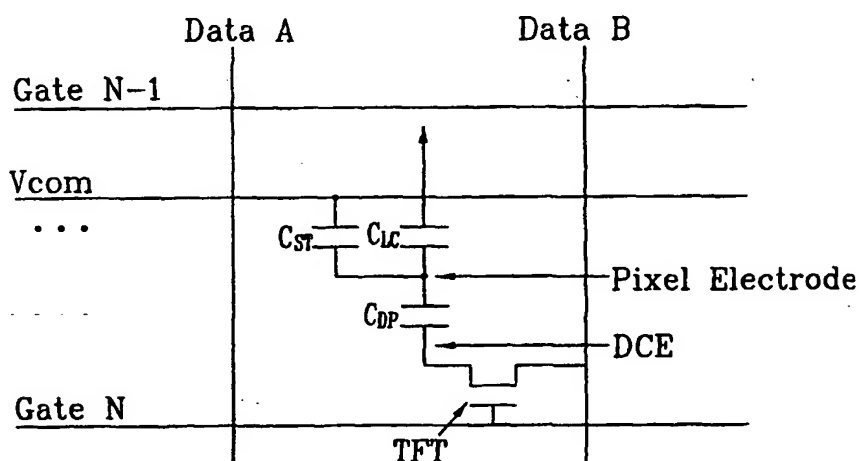
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(54) Title: MULTI-DOMAIN LIQUID CRYSTAL DISPLAY AND A THIN FILM TRANSISTOR SUBSTRATE OF THE SAME



(57) Abstract: Gate lines are formed on an insulating substrate, and data lines crossing the gate lines are formed. The gate lines and the data lines are insulated from each other and intersect each other to define pixel areas. A thin film transistor including three terminals of a gate electrode, a source electrode and a drain electrode is formed in each pixel area. A direction control electrode and a pixel electrode are also formed in each pixel area. The thin film transistor switches the direction control electrode. The pixel electrode is electronically floating and capacitively coupled with the direction control electrode.

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